

Fig. 1

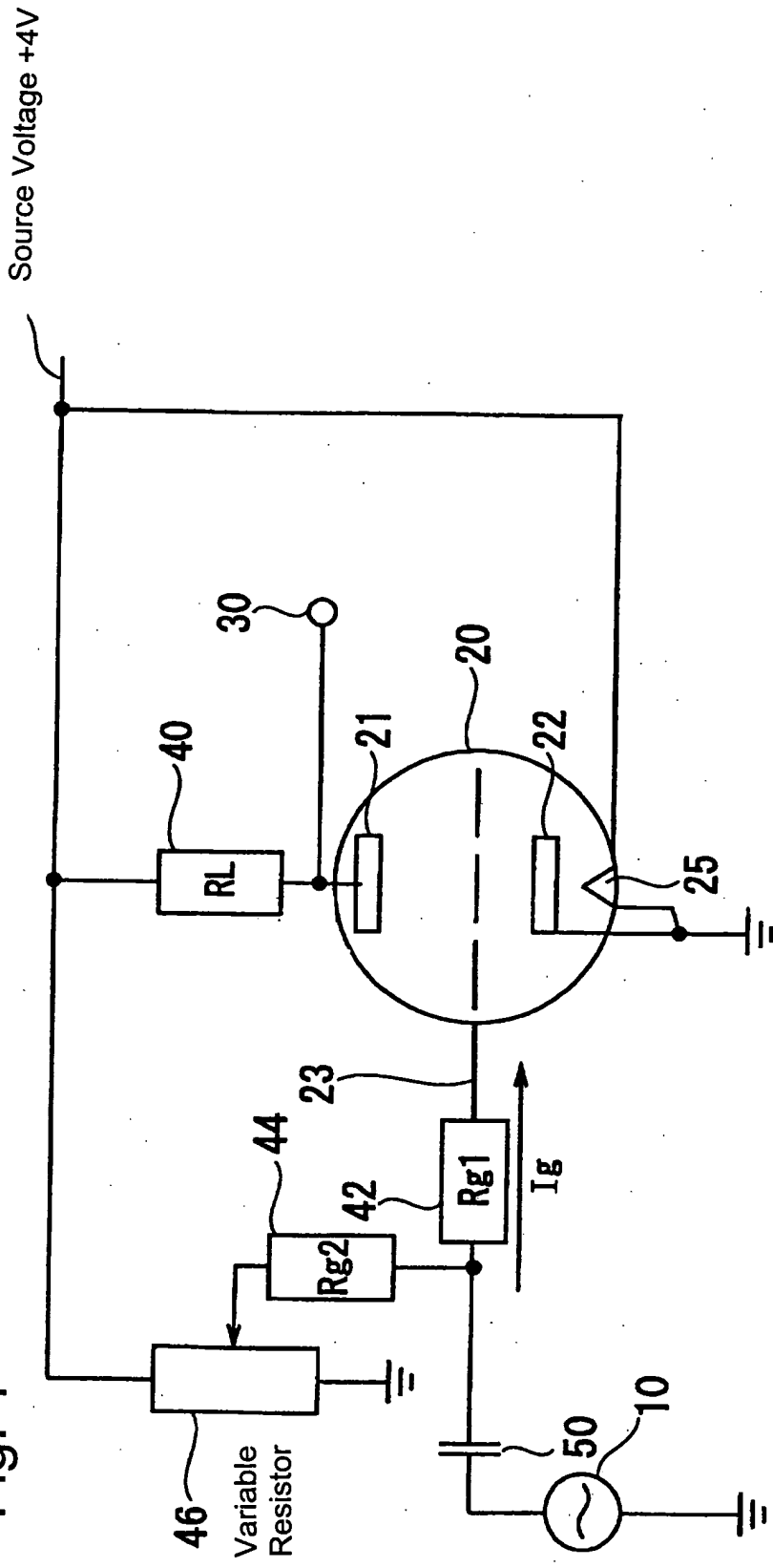


Fig. 2

The diagram shows a circuit for a photoconductive cell (20). A source voltage of +4V is applied to the left terminal. The photoconductive cell (20) is connected to the source voltage and a load resistor (RL). The current through the load resistor is labeled I_p . The photoconductive cell (20) is also connected to a feedback loop (60) and a readout circuit (65). The feedback loop (60) includes a resistor (R1) and a capacitor (C1) in series, and a resistor (R2) and a capacitor (C2) in parallel. The readout circuit (65) includes a resistor (R3) and a capacitor (C3) in series. The output of the readout circuit is connected to a terminal (30). The photoconductive cell (20) is also connected to a terminal (25) which is grounded. The photoconductive cell (20) is also connected to a terminal (23) which is connected to a resistor (Rg1) and a terminal (10) which is grounded. The photoconductive cell (20) is also connected to a terminal (21) which is connected to a resistor (R1) and a terminal (A) which is grounded. The photoconductive cell (20) is also connected to a terminal (22) which is connected to a resistor (R1) and a terminal (B) which is connected to the feedback loop (60).

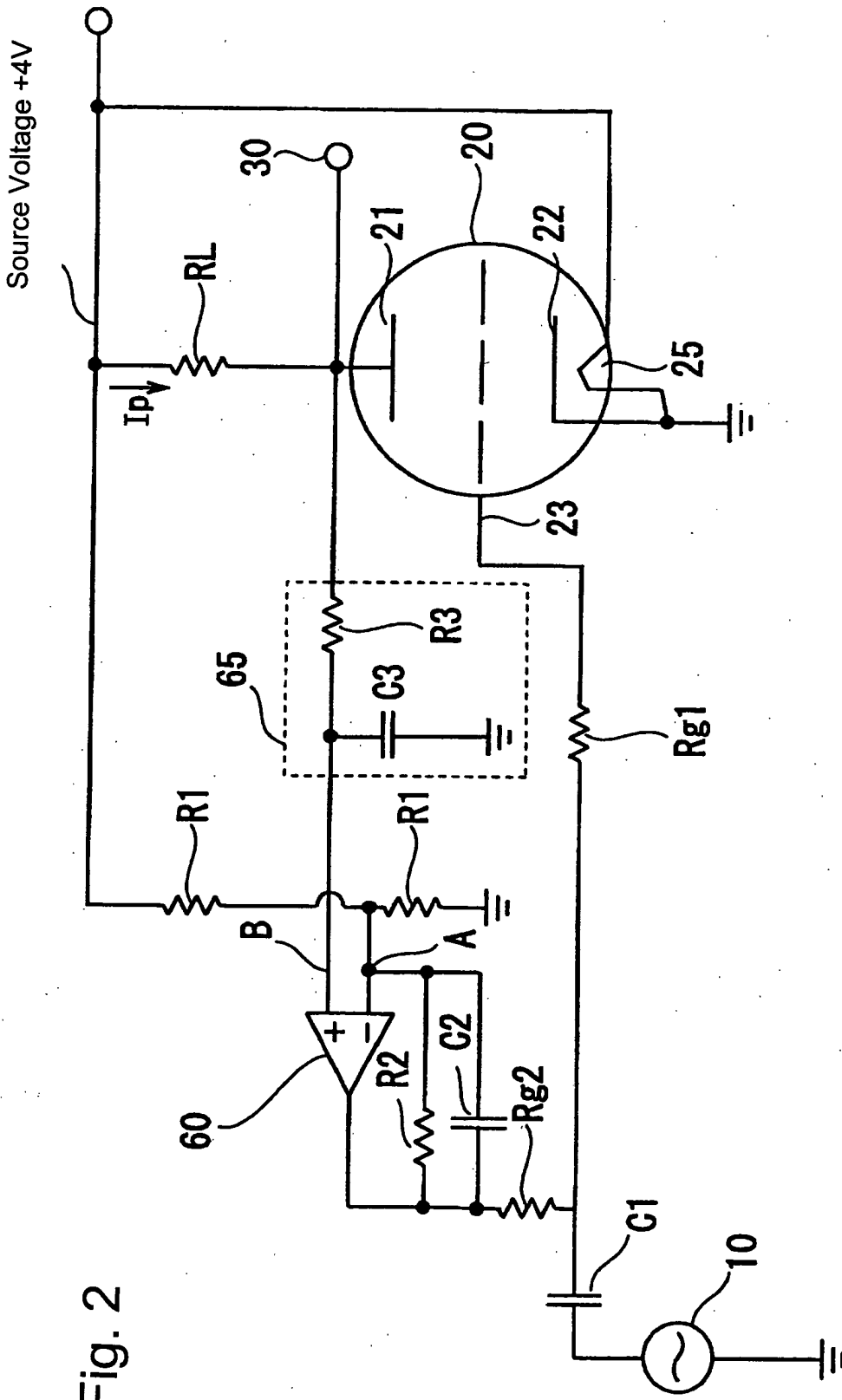


Fig. 3

